



Attorney's Docket No.: 42390.P5578
Confirmation No.: 5730

Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Brian S. Doyle

Application No.: 09/229,592

Filed: January 13, 1999

For: TRANSISTOR WITH REDUCED
SERIES RESISTANCE JUNCTION
REGIONS

Examiner: Brock II, Paul E.

Art Unit: 2815

Mail Stop: RCE
Commissioner For Patents
P. O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT AND RESPONSE TO OFFICE ACTION

Dear Sir:

In response to the Final Office Action dated December 10, 2004, Applicant respectfully requests that the above-identified application be amended as follows and that the following remarks be considered:

FIRST-CLASS CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first-class mail with sufficient postage in an envelope addressed to Mail Stop: RCE, Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on:

February 17, 2005

Date of Deposit

Esther Campbell

Name of Person Mailing Correspondence

Esther Campbell
Signature

February 17, 2005

Date

Inventor(s): Brian S. Doyle, et al.
Application No.: 09/229,592

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